U.S. UTILITY Patent Application

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APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10005062	12/03/2001	76	16	2816	Garlowski

APPLICANTS: Watkins Daniel;   2825

CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH ☒	RESCIND ☐	
Foreign priority claimed	☐ yes ☐ no	**ATTORNEY DOCKET NO**	
35 USC 119 conditions met	☐ yes ☐ no		
Verified and Acknowledged Examiners's intials		LS1-01-651	

TITLE : Method and system for implementing incremental change to circuit design

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
	Assistant Examiner	Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig.
	Primary Examiner		
☐ TERMINAL DISCLAIMER	PREPARED FOR ISSUE	Application Examiner	

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